




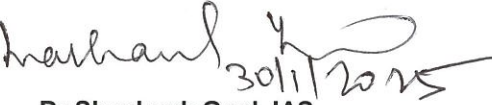
Dr. MCR HUMAN RESOURCE DEVELOPMENT INSTITUTE OF TELANGANA
Induction Training Program for Newly Recruited Examiners of Patents and Designs
under the Office of Controller General of Patents, Designs and Trade Marks (CGPDTM)
20th January to 3rd May 2025



WEEK - 3 (SESSION PLAN) - SECTION - I & II

03rd February 2025 2025 to 08th February 2025

TIME	Section	Monday Feb-03	Tuesday Feb-04	Wednesday Feb-05	Thursday Feb-06	Friday Feb-07	Saturday Feb-08	Sunday Feb-09	
9.30 AM to 11.15 AM	I	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (THEORY)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (THEORY)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (THEORY)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (THEORY)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (THEORY)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (THEORY)		
	II	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (THEORY)		
11.15 AM to 11.30 AM	Tea Break								
11.30 AM to 01.15 PM	I	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (THEORY)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (THEORY)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (THEORY)		
	II	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (THEORY)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (THEORY)		
1.15 PM to 2.15 PM	Lunch Break							HOLIDAY	HOLIDAY
2.15 PM to 4.00 PM	I	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (COMPUTER LAB)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (COMPUTER LAB)		
	II	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (COMPUTER LAB)		
4.00 PM to 4.15 PM	Tea Break								
4.15 PM to 06.00 PM	I	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Hariom Singh, Asst. Controller Mr. Ranjan Biswas, Asst. Controller (COMPUTER LAB)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (COMPUTER LAB)	Mr. Ajay Kumar Yadav, Asst. Controller Mr. Shubhanshu Gautam, Asst. Controller (COMPUTER LAB)		
	II	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller, Mr. Vipin Jain, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (COMPUTER LAB)	Mr. Vishal Shukla, Asst. Controller Mr. Anil Tagale, Asst. Controller Mr. Gaurav Kumar Tomar, Asst. Controller (COMPUTER LAB)		
Venues	I & II	Class Rooms Sessions: Plenary Session (Section-I & II) : Room No. 222, Admin Building (2nd Floor) (Section - I) : Room No. 222, Admin Building (2nd Floor) (Section - II) : Room No. 219, Admin Building (2nd Floor)				Lab Sessions: Section - I ELECTRONICS & COMMUNICATION (ECE) Class Room No.123, 01st Floor, Admin Building Section - II COMPUTER SCIENCE & INFORMATION TECHNOLOGY (CS & IT) Computer Lab, Opp. Ground Floor, Thugabhadra Building			

ACADEMIC INPUTS	SPEAKERS
Structure of Patent specification and its understanding - Theory session	<p style="text-align: center;">Mr. Hariom Singh, Asst. Controller</p> <p style="text-align: center;">&</p> <p style="text-align: center;">Mr. Ranjan Biswas, Asst. Controller</p>
Structure of Patent specification and its understanding - Practice session	
Structure of Patent specification and its understanding - Feedback session	
Understanding Claims - Theory session	<p style="text-align: center;">Mr. Ajay Kumar Yadav, Asst. Controller</p> <p style="text-align: center;">&</p> <p style="text-align: center;">Mr. Shubhanshu Gautam, Asst. Controller</p>
Understanding Claims - Practice session	
Understanding Claims - Feedback session	
Structure of Patent specification and its understanding - Theory session	<p style="text-align: center;">Mr. Vishal Shukla, Asst. Controller</p> <p style="text-align: center;">&</p> <p style="text-align: center;">Mr. Vipin Jain, Asst. Controller</p>
Structure of Patent specification and its understanding - Practice session	
Structure of Patent specification and its understanding - Feedback session	
Understanding Claims - Theory session	<p style="text-align: center;">Mr. Vishal Shukla, Asst. Controller</p> <p style="text-align: center;">&</p> <p style="text-align: center;">Mr. Anil Tagale, Asst. Controller</p> <p style="text-align: center;">&</p> <p style="text-align: center;">Mr. Gaurav Kumar Tomar, Asst. Controller</p>
Understanding Claims - Practice session	
Understanding Claims - Feedback session	
Lab Sessions - Room No's	MORNING ACTIVITY
<p>Section - I ELECTRONICS & COMMUNICATION (ECE) Class Room No.123, 01st Floor, Admin Building</p> <p>Section - II COMPUTER SCIENCE & INFORMATION TECHNOLOGY (CS & IT) Computer Lab, Opp. Ground Floor, Thugabhadra Building</p>	<p style="text-align: center;">Section - I & II</p> <p style="text-align: center;">PT</p> <p>Date: 03.02.2025 to 07.02.2025 (Monday to Friday) Time: 6.30am to 7.30am Venue: New Sports Ground (Football Court)</p>
 Dr. D. Thiruvengala Chary Course Director	 Dr. Shashank Goel, IAS Director General